

## WEST Search History for Application 10669948

Creation Date: 2010010217:34

### Prior Art Searches

Query	DB	Op.	Plur.	Thes.	Date
707/8 206.ccls.	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(711/105  711/151  711/152  711/155).ccls.	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(718/102  718/104  718/108).ccls.	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((718/102  718/104  718/108).ccls. ) or ((711/105  711/151  711/152  711/155).ccls. ) or (707/8 206.ccls. )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier).ab, ti.	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier).ab, ti. ) and (out near (order or sequence))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (free or avoid\$3 or without or no)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(out near (order or sequence)) with (cpu or processor)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((out near (order or sequence)) with (cpu or processor) ) and ((membar or mbar or memory near barrier) with (free or avoid\$3 or without or no) )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010

(membar or mbar or memory near barrier) with (free or avoid\$3 or without or no) and barriers	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier) with (free or avoid\$3 or without or no) and barriers ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier) with (free or avoid\$3 or without or no) and barriers and @ad<20020924 ) and thread	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) near (free or avoid\$3 or without)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier) near (free or avoid\$3 or without) ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
powerpc and (membar or mbar or memory near barrier)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(powerpc and (membar or mbar or memory near barrier) ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (acquir\$3 or releas\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier) with (acquir\$3 or releas\$3) ) and ((out near (order or sequence)) with (cpu or processor) )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((membar or mbar or memory near barrier) with (acquir\$3 or releas\$3) ) and ((718/102  718/104  718/108).ccls. or (711/105  711/151  711/152  711/155).ccls. or 707/8 206.ccls. )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (lock\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010

((membar or mbar or memory near barrier) with (lock\$3) ) and (out near (order or sequence))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (out near (order or sequence))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(barrier) with (out near (order or sequence))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((barrier) with (out near (order or sequence)) ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(acquir\$3 and releas\$3) with (out near (order or sequence)) near (processing or execut\$3 or instruction)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(acquir\$3 and releas\$3) with (out near (order or sequence))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (acquir\$3 and releas\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(re\$1order\$3) with (acquir\$3 and releas\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
memory near fenc\$3 with (re\$1order\$3 or (out near (order or sequence)))	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(membar or mbar or memory near barrier) with (re\$1order\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((barrier) with (out near (order or sequence)) ) and ((718/102  718/104  718/108).ccls. or (711/105  711/151  711/152  711/155).ccls. or 707/8 206.ccls. )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010

((membar or mbar or memory near barrier) with (re\$1order\$3) ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
memory near fence\$3 with (free or avoid\$3 or without)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((718/102  718/104  718/108).ccls. or (711/105  711/151  711/152  711/155).ccls. or 707/8 206.ccls. ) and ((out near (order or sequence)) with (cpu or processor) )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
re\$1order\$3 with (cpu or processor)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(re\$1order\$3 with (cpu or processor) ) and (membar or mbar or memory near barrier)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(re\$1order\$3 with (cpu or processor) and (membar or mbar or memory near barrier) ) and @ad<20020924	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
(acquir\$3 or releas\$3) with lock with (re\$1order\$3 or order\$3)	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010
((acquir\$3 or releas\$3) with lock with (re\$1order\$3 or order\$3) ) and ((out near (order or sequence)) with (cpu or processor) )	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD	OR	YES		01-02-2010